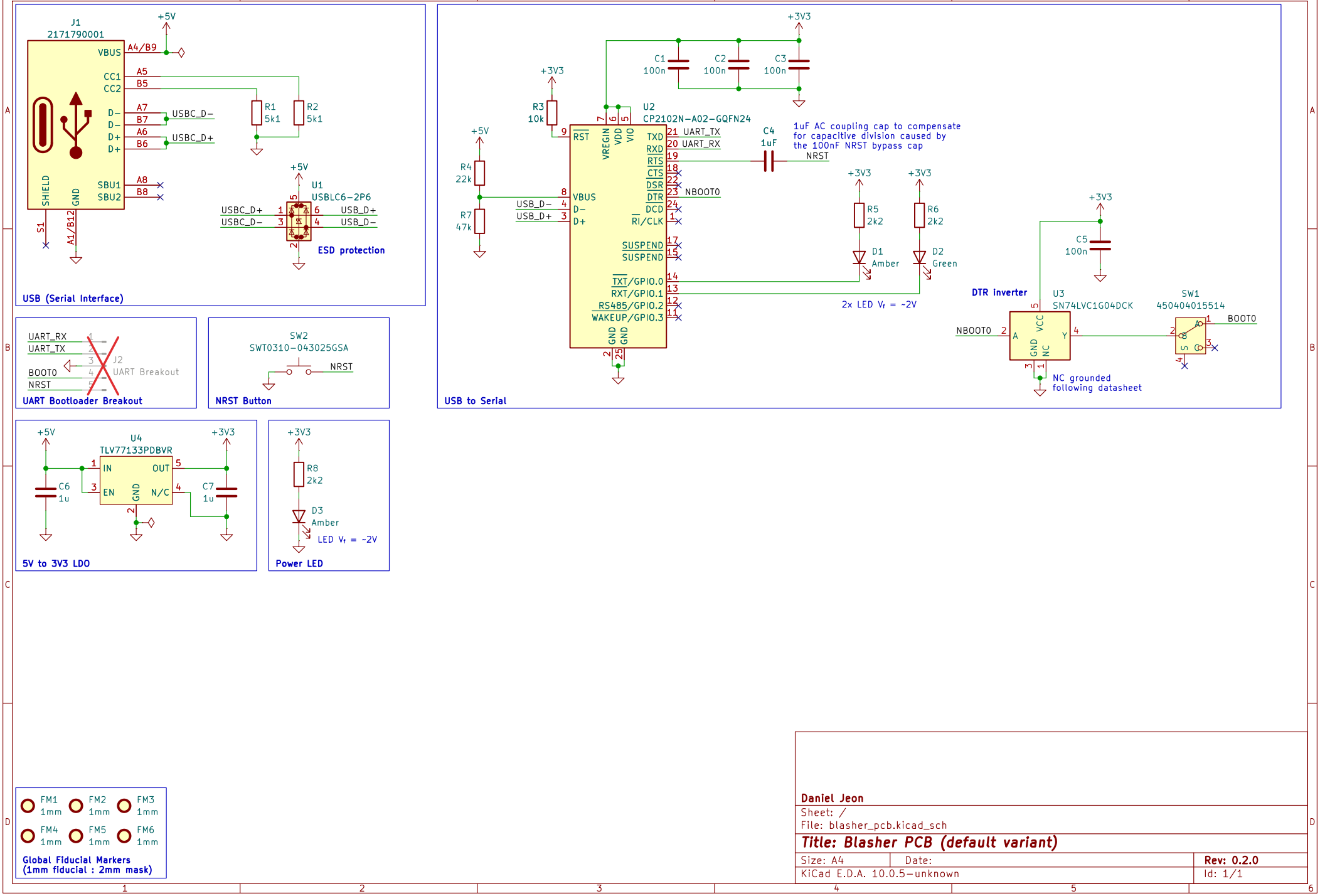

J1
2171790001
VBUS
A4/B9
CC1
A5
CC2
B5
D−
A7
D−
B7
D+
A6
D+
B6
SHIELD
GND
SBU1
A8
SBU2
B8
S1
A1/B12
USBC_D−
USBC_D+
R1
5k1
R2
5k1
+5V
U1
USBLC6−2P6
USBC_D+
USBC_D−
USB_D+
USB_D−
ESD protection
USB (Serial Interface)
UART_RX
UART_TX
BOOT0
NRST
J2
UART Breakout
UART Bootloader Breakout
SW2
SWT0310−043025GSA
NRST
NRST Button
+3V3
C1
100n
C2
100n
C3
100n
R3
10k
U2
CP2102N−A02−GQFN24
RST
VREGIN
VDD
VIO
TXD
UART_TX
RXD
UART_RX
RTS
CTS
DSR
DTR
NBOOT0
DCD
RI/CLK
SUSPEND
SUSPEND
TXT/GPIO.0
RXT/GPIO.1
RS485/GPIO.2
WAKEUP/GPIO.3
GND
GND
VBUS
D−
D+
USB_D−
USB_D+
R4
22k
R7
47k
C4
1uF
1uF AC coupling cap to compensate for capacitive division caused by the 100nF NRST bypass cap
NRST
R5
2k2
R6
2k2
D1
Amber
D2
Green
2x LED Vf = ~2V
DTR Inverter
U3
SN74LVC1G04DCK
NBOOT0
BOOT0
C5
100n
NC grounded following datasheet
SW1
450404015514
USB to Serial
U4
TLV77133PDBVR
IN
OUT
EN
GND
N/C
C6
1u
C7
1u
5V to 3V3 LDO
R8
2k2
D3
Amber
LED Vf = ~2V
Power LED
FM1
1mm
FM2
1mm
FM3
1mm
FM4
1mm
FM5
1mm
FM6
1mm
Global Fiducial Markers
(1mm fiducial : 2mm mask)
Daniel Jeon
Sheet: /
File: blasher_pcb.kicad_sch
Title: Blasher PCB (default variant)
Size: A4
Date:
Rev: 0.2.0
KiCad E.D.A. 10.0.5−unknown
Id: 1/1